

EXAR UART and I/O SELECTOR GUIDE (Rev 2.20, August 2006)

UART DEVICE	Channels	Max Data Rate (Mbps)	TX/RX FIFO Bytes	TX & RX FIFO Level Counters	RX/TX FIFO INT Trigger	RTS/CTS Flow Control	S/W Flow Control	IrDA ver1.0 Endec	Auto RS485 HDX Control	Data Bus Interface	Power Down Mode	Max Icc (mA)	Packages	Other Features
66MHz, 3.3V, 32-BIT PCI BUS UARTs with 5th GENERATION FLAT REGISTER SET														
XR17V258	8	8.0 @ 3.3V	64 / 64	Yes	Program / Program	Auto	Auto	Yes, with enable pin	Yes, with prog. delay	PCI Bus 3.0	Auto Sleep	4.0	LQFP-144	66MHz 3.3V PCI bus 3.0 compliant interface, 32-bit wide data loading and unloading, Power Management 1.1 compliance, global interrupt source register, 5G (flat) register set, fractional baud rate generator, 8X mode, RTS/CTS or DTR/DSR flow control, auto RS485 HDX direction control output with programmable delay.
XR17V254*	4	8.0 @ 3.3V	64 / 64	Yes	Program / Program	Auto	Auto	Yes, with enable pin	Yes, with prog. delay	PCI Bus 3.0	Auto Sleep	4.0	LQFP-144	66MHz 3.3V PCI bus 3.0 compliant interface, 32-bit wide data loading and unloading, Power Management 1.1 compliance, global interrupt source register, 5G (flat) register set, fractional baud rate generator, 8X mode, RTS/CTS or DTR/DSR flow control, auto RS485 HDX direction control output with programmable delay.
XR17V252	2	8.0 @ 3.3V	64 / 64	Yes	Program / Program	Auto	Auto	Yes, with enable pin	Yes, with prog. delay	PCI Bus 3.0	Auto Sleep	4.0	TQFP-100	66MHz 3.3V PCI bus 3.0 compliant interface, 32-bit wide data loading and unloading, Power Management 1.1 compliance, global interrupt source register, 5G (flat) register set, fractional baud rate generator, 8X mode, RTS/CTS or DTR/DSR flow control, auto RS485 HDX direction control output with programmable delay.
33MHz, UNIVERSAL (3.3V or 5V), 32-BIT PCI BUS UARTs with 5th GENERATION FLAT REGISTER SET														
XR17D158	8	6.25 @ 5V 6.25 @ 3.3V	64 / 64	Yes	Program / Program	Auto	Auto	Yes, with enable pin	Yes, with prog. delay	PCI Bus 2.2	Auto Sleep	4.0	LQFP-144	33MHz, 3.3V and 5V PCI bus 2.2 compliant interface, 32-bit wide data loading and unloading, global interrupt source register, 5G (flat) register set, 8X mode, RTS/CTS or DTR/DSR flow control, auto RS485 HDX direction control output with programmable delay.
XR17D154	4	6.25 @ 5V 6.25 @ 3.3V	64 / 64	Yes	Program / Program	Auto	Auto	Yes, with enable pin	Yes, with prog. delay	PCI Bus 2.2	Auto Sleep	3.0	LQFP-144	Four channel version of XR17D158 in the same package and pin-out.
XR17D152	2	6.25 @ 5V 6.25 @ 3.3V	64 / 64	Yes	Program / Program	Auto	Auto	Yes, with enable pin	Yes, with prog. delay	PCI Bus 2.2	Auto Sleep	3.0	TQFP-100	Two channel version of XR17D158
33MHz, 5V, 32-BIT PCI BUS UARTs with 5th GENERATION FLAT REGISTER SET														
XR17C158	8	6.25 @ 5V	64 / 64	Yes	Program / Program	Auto	Auto	Yes, with enable pin	Yes, with prog. delay	PCI Bus 2.2	Auto Sleep	5.0	LQFP-144	5V only version of XR17D158 in the same package and pin-out.
XR17C154	4	6.25 @ 5V	64 / 64	Yes	Program / Program	Auto	Auto	Yes, with enable pin	Yes, with prog. delay	PCI Bus 2.2	Auto Sleep	3.0	LQFP-144	Four channel version of XR17C158 in the same package and pin-out.
XR17C152	2	6.25 @ 5V	64 / 64	Yes	Program / Program	Auto	Auto	Yes, with enable pin	Yes, with prog. delay	PCI Bus 2.2	Auto Sleep	3.0	TQFP-100	Two channel version of XR17C158
5th GENERATION ENHANCED 8-BIT BUS UARTs with SIMPLER HOST INTERFACE and FLAT REGISTERS														
XR16V798	8	8.0 @ 3.3V 6.25 @ 2.5V	64 / 64	Yes	Program / Program	Auto	Auto	Yes, with enable pin	Yes, with prog. delay	Intel or Motorola	Auto Sleep	5.0	QFP-100	2.25V - 3.6V octal UART, 8Mbps max data rate, global interrupt source register, flat register set, fractional baud rate generator, 8X Mode, RTS/CTS or DTR/DSR flow control, programmable RS485 HDX control output delay.
XR16V794*	4	8.0 @ 3.3V 6.25 @ 2.5V	64 / 64	Yes	Program / Program	Auto	Auto	Yes, with enable pin	Yes, with prog. delay	Intel or Motorola	Auto Sleep	5.0	LQFP-64	2.25V - 3.6V octal UART, 8Mbps max data rate, global interrupt source register, flat register set, fractional baud rate generator, 8X Mode, RTS/CTS or DTR/DSR flow control, programmable RS485 HDX control output delay.
XR16L788	8	6.25 @ 5V 4.0 @ 3.3V	64 / 64	Yes	Program / Program	Auto	Auto	Yes, with enable pin	Yes, with prog. delay	Intel or Motorola	Auto Sleep	5.0	QFP-100	2.97V - 5.5V octal UART, global interrupt source register, flat register set, 8X, RTS/CTS or DTR/DSR flow control, programmable RS485 HDX control output delay.
XR16L784	4	6.25 @ 5V 4.0 @ 3.3V	64 / 64	Yes	Program / Program	Auto	Auto	Yes, with enable pin	Yes, with prog. delay	Intel or Motorola	Auto Sleep	3.0	LQFP-64	Four channel version of the XR16L788
UART + TRANSCEIVER COMBO														
XR19L220*	1	0.25 @ 5V 0.25 @ 3.3V	16 / 16	No	4 Levels / 4 Levels	Auto	Auto	No	No	Intel or Motorola	Power Save	30	QFN-40	3.3V or 5V UART with RS-232 Transceiver Combo. 5V tolerant inputs. Software compatible to industry standard 16550, Intel or Motorola mode, PowerSave mode to further reduce power drain in sleep mode. Full-modem interface.
XR19L210*	1	0.25 @ 5V 0.25 @ 3.3V	16 / 16	No	4 Levels / 4 Levels	Auto	Auto	No	No	Intel or Motorola	Power Save	30	QFN-40	3.3V or 5V UART with RS-232 Transceiver Combo. 5V tolerant inputs. Software compatible to industry standard 16550, Intel or Motorola mode, PowerSave mode to further reduce power drain in sleep mode. Half-modem interface.
XR19L200*	1	0.25 @ 5V 0.25 @ 3.3V	16 / 16	No	4 Levels / 4 Levels	Auto	No	No	No	Intel or Motorola	Power Save	30	QFN-32	3.3V or 5V UART with RS-232 Transceiver Combo. 5V tolerant inputs. Software compatible to industry standard 16550, Intel or Motorola mode, PowerSave mode to further reduce power drain in sleep mode. Quarter-modem interface.
XR19L400*	1	8 @ 5V 8 @ 3.3V	64 / 64	Yes	Program / Program	No	No	Yes	Yes	Intel or Motorola	Power Save	30	QFN-40	3.3V or 5V UART with RS-485/RS-422 Transceiver Combo. 5V tolerant inputs. Software compatible to industry standard 16550, Intel or Motorola mode, PowerSave mode to further reduce power drain in sleep mode.
XR19L222*	2	1 @ 5V 1 @ 3.3V	64 / 64	Yes	Program / Program	Auto	Auto	Yes	Yes	Intel or Motorola	Power Save	30	QFN-64	3.3V or 5V Dual UART with RS-232 Transceiver Combo. 5V tolerant inputs. Software compatible to industry standard 16550, Intel or Motorola mode, PowerSave mode to further reduce power drain in sleep mode. Full-modem interface.
XR19L212*	2	1 @ 5V 1 @ 3.3V	64 / 64	Yes	Program / Program	Auto	Auto	Yes	Yes	Intel or Motorola	Power Save	30	QFN-48	3.3V or 5V Dual UART with RS-232 Transceiver Combo. 5V tolerant inputs. Software compatible to industry standard 16550, Intel or Motorola mode, PowerSave mode to further reduce power drain in sleep mode. Half-modem interface.
XR19L202*	2	1 @ 5V 1 @ 3.3V	64 / 64	Yes	Program / Program	Auto	No	Yes	Yes	Intel or Motorola	Power Save	30	QFN-48	3.3V or 5V Dual UART with RS-232 Transceiver Combo. 5V tolerant inputs. Software compatible to industry standard 16550, Intel or Motorola mode, PowerSave mode to further reduce power drain in sleep mode. Quarter-modem interface.
XR19L402*	2	8 @ 5V 8 @ 3.3V	64 / 64	Yes	Program / Program	No	No	Yes	Yes	Intel or Motorola	Power Save	30	QFN-48	3.3V or 5V Dual UART with RS-485/RS-422 Transceiver Combo. 5V tolerant inputs. Software compatible to industry standard 16550, Intel or Motorola mode, PowerSave mode to further reduce power drain in sleep mode.
I2C/SPI UART														
XR20M1170*	1	16.0 @ 3.3V 12.5 @ 2.5V	64 / 64	Yes	Program / Program	Auto	Auto	Yes	Yes	I2C / SPI	Auto Sleep	3.0	TSSOP-16 QFN-16 TSSOP-24 QFN-24 QFN-28	2.25 - 3.63V UART with selectable I2C/SPI bus interface.

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STANDARD 8-BIT BUS UARTs														
XR16C864	4	2.0 @ 5V 1.5 @ 3.3V	128 / 128	Yes	Program / Program	Auto	Auto	Yes	Auto	Intel or Motorola	Auto Sleep	6.0	QFP-100	Same as 854 with auto RS485 half-duplex direction control output, TXRDY (A-D) and RXRDY (A-D) outputs.
XR16C854 / 854D	4	2.0 @ 5V 1.5 @ 3.3V	128 / 128	Yes	Program / Program	Auto	Auto	Yes	Auto	Intel or Motorola	Auto Sleep	6.0	PLCC-68 LQFP-64 QFP-100	Similar to 654 with 128 byte FIFOs, programmable FIFO level triggers, FIFO level counters and 16-level RTS flow control hysteresis.
XR16V654 / 654D*	4	16.0 @ 3.3V 12.5 @ 2.5V	64 / 64	No	4 Levels / 4 Levels	Auto	Auto	Yes	No	Intel or Motorola	Auto Sleep	6.0	QFN-48 PLCC-68 LQFP-64 LQFP-80 QFP-100	Similar to ST16C654 with lower operating voltage (2.25 - 3.63V), 5V tolerant inputs, fractional baud rate generator and higher maximum data rate.
ST16C654 / 654D	4	1.5 @ 5V 0.5 @ 3.3V	64 / 64	No	4 Levels / 4 Levels	Auto	Auto	Yes	No	Intel or Motorola	Auto Sleep	6.0	PLCC-68 LQFP-64 QFP-100	Similar to 554/454 with 64 byte FIFOs, automatic flow control, separate Baudout clock input (CHCCLK) for channel-C (100-pin package), 1X or 4X BRG prescaler select pin (CLKSEL). The 654D provides active interrupt output instead of interrupt disable in the 654. The 654D is comparable to TL16C754B.
XR16V564 / 564D*	4	16.0 @ 3.3V 12.5 @ 2.5V	32 / 32	No	4 Levels / 4 Levels	Auto	Auto	Yes	No	Intel or Motorola	Auto Sleep	6.0	QFN-48 PLCC-68 LQFP-64 LQFP-80	2.25V - 3.63V quad UART with 32-byte FIFO, 5V tolerant inputs, auto HW and SW flow control, IR mode, automatic sleep mode, and fractional baud rate generator. Pin compatible to XR16V554 and XR16V654.
XR16V554 / 554D*	4	8.0 @ 3.3V 6.25 @ 2.5V	16 / 16	No	4 Levels / No	No	No	No	No	Intel or Motorola	No	3.0	QFN-48 PLCC-68 LQFP-64 LQFP-80	Similar to ST16C554 with lower operating voltage (2.25 - 3.63V), 5V tolerant inputs, fractional baud rate generator and higher maximum data rate.
ST16C554 / 554D	4	1.5 @ 5V 0.5 @ 3.3V	16 / 16	No	4 Levels / No	No	No	No	No	Intel or Motorola	No	3.0	PLCC-68 LQFP-64	Same as 454 but with 16 byte FIFOs. The 554D provides active interrupt outputs and comparable to TL16C554A.
ST68C554	4	1.5 @ 5V 0.5 @ 3.3V	16 / 16	No	4 Levels / No	No	No	No	No	Motorola	No	3.0	PLCC-68	Same as ST16C554/554D with pin 31 tied to ground externally.
ST16C454	4	1.5 @ 5V 0.5 @ 3.3V	1 / 1	No	No / No	No	No	No	No	Intel or Motorola	No	3.0	PLCC-68	2.97V - 5.5V quad UART. Four 450's with individual channel select and interrupt request lines.
XR82C684	4	1.0 @ 5V	3 / 3	No	No / No	No	No	No	No	Intel or Motorola	Standby	6.0	PLCC-44 PLCC-68	5V only quad UART. Has select pin for 68 or 88 mode, ext. 1X or 16X clock, 115.2Kbps at 3.6864 MHz, multidrop (9th bit) mode, and 16 I/O pins and uses the timer/counter for FIFO trigger.
XR16C2850	2	6.25 @ 5V 4.0 @ 3.3V	128 / 128	Yes	Program / Program	Auto	Auto	Yes	Auto	Intel	Auto Sleep	3.0	PLCC-44 TQFP-48	2.97V - 5.5V Dual UART with 5V tolerant inputs. Compatible to 2750, 2550 and 2450 with 128-byte FIFO's, auto RS485 half-duplex direction control output and enhanced features.
XR16C2852	2	6.25 @ 5V 4.0 @ 3.3V	128 / 128	Yes	Program / Program	Auto	Auto	Yes	Auto	Intel	Auto Sleep	3.0	PLCC-44	2.97V - 5.5V Dual UART with 5V tolerant inputs. Pin compatible to 2752, 2552 and National PC16552 with 128-byte FIFO's, auto RS485 half-duplex direction control output and enhanced features.
XR16V2750	2	8.0 @ 3.3V 6.25 @ 2.5V	64 / 64	Yes	Program / Program	Auto	Auto	Yes	Auto	Intel	Auto Sleep	3.0	TQFP-48 QFN-32	2.25V - 3.63V Dual UART with 5V tolerant inputs. Pin compatible to 2850, 2550 and 2450 with 64-byte FIFO's, auto HW and SW flow control, auto RS485 half-duplex direction control output, fractional baud rate generator and other enhanced features.
XR16V2751	2	8.0 @ 3.3V 6.25 @ 2.5V	64 / 64	Yes	Program / Program	Auto	Auto	Yes	Yes, with enable pin,	Intel or Motorola	Power Save	3.0	TQFP-48	2.25V - 3.63V Dual UART with 5V tolerant inputs. Same as V2750 plus Motorola bus interface (16/68# pin), has PowerSave for Sleep mode to cut power drain and a AutoRS485 global enable pin, HDCNTL#.
XR16V2752*	2	8.0 @ 3.3V 6.25 @ 2.5V	64 / 64	Yes	Program / Program	Auto	Auto	Yes	Auto	Intel	Auto Sleep	3.0	PLCC-44 QFN-32	2.25V - 3.63V Dual UART with 5V tolerant inputs. Pin compatible to 2852, 2552 and National PC16552 with 64-byte FIFO's, fractional baud rate generator and other enhanced features.
XR16M752*	2	16.0 @ 3.3V 12.5 @ 2.5V	64 / 64	Yes	Program / Program	Auto	Auto	Yes	Auto	Intel	Auto Sleep	3.0	TQFP-48 QFN-32	1.62V - 3.63V Dual UART with 5V tolerant inputs. Pin and software compatible to TL16C752B and SC16C752B. Enhanced features include auto RS485 half-duplex direction control output, fractional baud rate generator and higher maximum data rate.
XR68M752*	2	8.0 @ 3.3V 6.25 @ 2.5V	64 / 64	Yes	Program / Program	Auto	Auto	Yes	Auto	Intel or Motorola	Auto Sleep	3.0	TQFP-48 QFN-32	1.62V - 3.63V Dual UART with 5V tolerant inputs. Same as M752 plus Motorola bus interface (16/68# pin). Pin and software compatible to SC68C752B. Enhanced features include auto RS485 half-duplex direction control output, fractional baud rate generator and higher maximum data rate.
XR16L2750	2	6.0 @ 5V 4.0 @ 3.3V 3.0 @ 2.5V	64 / 64	Yes	Program / Program	Auto	Auto	Yes	Auto	Intel	Auto Sleep	3.0	PLCC-44 TQFP-48	2.25V - 5.5V Dual UART with 5V tolerant inputs. Pin compatible to 2850, 2550 and 2450 with 64-byte FIFO's, auto RS485 half-duplex direction control output and other enhanced features.
XR16L2751	2	6.0 @ 5V 4.0 @ 3.3V 3.0 @ 2.5V	64 / 64	Yes	Program / Program	Auto	Auto	Yes	Yes, with enable pin,	Intel or Motorola	Power Save	3.0	TQFP-48	2.25V - 5.5V Dual UART with 5V tolerant inputs. Same as L2750 plus Motorola bus interface (16/68# pin), has PowerSave for Sleep mode to cut power drain and a AutoRS485 global enable pin, HDCNTL#.
XR16L2752	2	6.0 @ 5V 4.0 @ 3.3V 3.0 @ 2.5V	64 / 64	Yes	Program / Program	Auto	Auto	Yes	Auto	Intel	Auto Sleep	3.0	PLCC-44	2.25V - 5.5V Dual UART with 5V tolerant inputs. Pin compatible to 2852, 2552 and National PC16552 with 64-byte FIFO's and other enhanced features.
XR16V2650*	2	16.0 @ 3.3V 12.5 @ 2.5V	32 / 32	Yes	4 Levels / 4 Levels	Auto	Auto	Yes	No	Intel	Auto Sleep	3.0	TQFP-48 QFN-32	2.25V - 3.63V Dual UART with 5V tolerant inputs. Pin compatible to 2850, 2550 and 2450 with 32-byte FIFO's, auto HW and SW flow control, fractional baud rate generator and other enhanced features.
XR16V2651*	2	16.0 @ 3.3V 12.5 @ 2.5V	32 / 32	Yes	4 Levels / 4 Levels	Auto	Auto	Yes	No	Intel or Motorola	Power Save	3.0	TQFP-48 QFN-32	2.25V - 3.63V Dual UART with 5V tolerant inputs. Same as V2650 plus Motorola bus interface (16/68# pin), has PowerSave for Sleep mode to cut power drain.
XR16V2652*	2	16.0 @ 3.3V 12.5 @ 2.5V	32 / 32	Yes	4 Levels / 4 Levels	Auto	Auto	Yes	No	Intel	Auto Sleep	3.0	PLCC-44 QFN-32	2.25V - 3.63V Dual UART with 5V tolerant inputs. Pin compatible to 2852, 2552 and National PC16552 with 32-byte FIFO's, fractional baud rate generator and other enhanced features.

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XR16M2650*	2	16.0 @ 3.3V 12.5 @ 2.5V	32 / 32	Yes	4 Levels / 4 Levels	Auto	Auto	Yes	No	Intel	Auto Sleep	3.0	TQFP-48 QFN-32	1.62V - 3.63V Dual UART. Pin compatible to 2850, 2550 and 2450 with 32-byte FIFO's, auto HW and SW flow control, fractional baud rate generator and other enhanced features.
XR16M2651*	2	16.0 @ 3.3V 12.5 @ 2.5V	32 / 32	Yes	4 Levels / 4 Levels	Auto	Auto	Yes	No	Intel or Motorola	Power Save	3.0	TQFP-48 QFN-32	1.62V - 3.63V Dual UART. Same as M2650 plus Motorola bus interface (16/68# pin), has PowerSave for Sleep mode to cut power drain.
XR16V2550*	2	16.0 @ 3.3V 12.5 @ 2.5V	16 / 16	Yes	4 Levels / 4 Levels	Auto	Auto	Yes	No	Intel	Auto Sleep	3.0	TQFP-48 QFN-32	2.25V - 3.63V Dual UART with 5V tolerant inputs. Pin compatible to 2850, 2550 and 2450 with 16-byte FIFO's, auto HW and SW flow control, fractional baud rate generator and other enhanced features.
XR16V2551*	2	16.0 @ 3.3V 12.5 @ 2.5V	16 / 16	Yes	4 Levels / 4 Levels	Auto	Auto	Yes	No	Intel or Motorola	Power Save	3.0	TQFP-48 QFN-32	2.25V - 3.63V Dual UART with 5V tolerant inputs. Same as V2550 plus Motorola bus interface (16/68# pin), has PowerSave for Sleep mode to cut power drain.
XR16V2552*	2	16.0 @ 3.3V 12.5 @ 2.5V	16 / 16	Yes	4 Levels / 4 Levels	Auto	Auto	Yes	No	Intel	Auto Sleep	3.0	PLCC-44 QFN-32	2.25V - 3.63V Dual UART with 5V tolerant inputs. Pin compatible to ST16C2552 and National PC16552 with 16-byte FIFO's, fractional baud rate generator and other enhanced features.
XR16M2550*	2	16.0 @ 3.3V 12.5 @ 2.5V	16 / 16	Yes	4 Levels / 4 Levels	Auto	Auto	Yes	No	Intel	Auto Sleep	3.0	TQFP-48 QFN-32	1.62V - 3.63V Dual UART. Pin compatible to 2850, 2550 and 2450 with 16-byte FIFO's, auto HW and SW flow control, fractional baud rate generator and other enhanced features.
XR16M2551*	2	16.0 @ 3.3V 12.5 @ 2.5V	16 / 16	Yes	4 Levels / 4 Levels	Auto	Auto	Yes	No	Intel or Motorola	Power Save	3.0	TQFP-48 QFN-32	1.62V - 3.63V Dual UART. Same as M2550 plus Motorola bus interface (16/68# pin), has PowerSave for Sleep mode to cut power drain.
XR16L2550	2	4.0 @ 5V 2.0 @ 3.3V 1.0 @ 2.5V	16 / 16	No	4 Levels / No	Auto	Auto	Yes	No	Intel	Auto Sleep	3.0	PLCC-44 TQFP-48 QFN-32	2.25V - 5.5V Dual UART with 5V tolerant inputs. Pin and software compatible to ST16C2550, tiny 32-QFN package and other enhanced features.
XR16L2551	2	4.0 @ 5V 2.0 @ 3.3V 1.0 @ 2.5V	16 / 16	No	4 Levels / No	Auto	Auto	Yes	No	Intel or Motorola	Power Save	3.0	TQFP-48 QFN-32	2.25V - 5.5V Dual UART with 5V tolerant inputs. Pin and software compatible to ST16C2550, Motorola mode, has PowerSave to reduce power drain in Sleep Mode further, tiny 32-QFN package and other enhanced features.
XR16L2552	2	4.0 @ 5V 2.0 @ 3.3V 1.0 @ 2.5V	16 / 16	No	4 Levels / No	Auto	Auto	Yes	No	Intel	Auto Sleep	3.0	PLCC-44 TQFP-48	2.25V - 5.5V Dual UART with 5V tolerant inputs. Pin compatible to ST16C2552 in PLCC package, 48-TQFP package and other enhanced features.
ST16C2550	2	4.0 @ 5V 1.875 @ 3.3V	16 / 16	No	4 Levels / No	No	No	No	No	Intel	No	1.2	PDIP-40 PLCC-44 TQFP-48	2.97V - 5.5V Dual UART. Pin-to-pin and function compatible to 2450 plus 16 FIFO and RX FIFO level trigger select. PDIP package Not Recommended for new designs. Use TQFP package instead.
ST16C2552	2	4.0 @ 5V 1.875 @ 3.3V	16 / 16	No	4 Levels / No	No	No	No	No	Intel	No	1.2	PLCC-44	2.97V - 5.5V Dual UART. National PC16552 replacement. Has channel A/B select pins, multi-function output pin for OP2, Baudout or RXRDY and concurrent register write to both channels.
XR16L2450	2	4.0 @ 5V 2.0 @ 3.3V 1.0 @ 2.5V	1 / 1	No	No / No	No	No	No	No	Intel	Auto Sleep	3.0	PLCC-44 TQFP-48	2.25V - 5.5V Dual UART with 5V tolerant inputs. Pin and software compatible to ST16C2450, tiny 32-QFN package and other enhanced features.
ST16C2450	2	1.5 @ 5V 0.5 @ 3.3V	1 / 1	No	No / No	No	No	No	No	Intel	No	1.2	PDIP-40 PLCC-44 TQFP-48	2.97V - 5.5V Dual UART. Two standard 450's. PDIP package Not Recommended for new designs. Use TQFP package instead.
ST16C552 / 16C552A	2S1P	1.5 @ 5V 0.5 @ 3.3V	16 / 16	No	4 Levels / No	No	No	No	No	Intel	Yes	1.2	PLCC-68	Same as 452. The 16C552 has the printer interrupt (INTP) signal being active "1". The 16C552A has the printer interrupt (INTP) signal being active "0" and is TL16C552 compatible.
ST16C452 / 16C452PS	2S1P	1.5 @ 5V 0.5 @ 3.3V	1 / 1	No	No / No	No	No	No	No	Intel	Yes	1.2	PLCC-68	Two 450's with a standard printer port. The 16C452 has a printer port direction control output. The 16C452PS provides additional software control bits for PS/2 compatibility.
XR68C681	2	1.0 @ 5V	1 / 3	No	No / No	Auto	No	No	No	Motorola	Standby	6.0	PLCC-44 PDIP-40 CDIP-40	5V UART. Motorola bus interface. Ext. 1X or 16X clock, 115.2Kbps at 3.6864MHz, separate TX and RX rates, multidrop (9th bit) mode and 8 general I/Os and uses the timer/counter for FIFO time-out trigger.
XR68/88C92	2	1.0 @ 5V 0.5 @ 3.3V	8 / 8	No	Yes / Yes	Auto	No	No	No	Intel or Motorola	Yes	6.0	PLCC-44 LQFP-44	2.97V - 5.5V Dual UART with 5V tolerant inputs. Intel I/F (88C92), Motorola I/F (68C92). Pin-to-pin and function compatible to the 68/88C681 plus 8 byte FIFOs and enhanced registers. PDIP package discontinued for XR88C92. Use TQFP instead.
XR68/88C192	2	1.0 @ 5V 0.5 @ 3.3V	16 / 16	No	Yes / Yes	Auto	No	No	No	Intel or Motorola	Yes	6.0	PLCC-44 LQFP-44	2.97V - 5.5V Dual UART with 5V tolerant inputs. Intel I/F (88C192), Motorola I/F (68C192). Pin-to-pin and function compatible to the 68/88C681 plus 16 byte FIFOs and enhanced registers.
XR88C681	2	1.0 @ 5V	1 / 3	No	No / No	Auto	No	No	No	Intel	Standby	6.0	PDIP-28 PLCC-44 PDIP-40 CDIP-40	5V UART. Intel bus I/F, ext. 1X or 16X clock, 115.2Kbps at 3.6864 MHz, separate TX and RX rates, multidrop (9th bit) mode, 8 general I/Os, and uses the timer/counter for FIFO time-out trigger.
XR16C850	1	2.0 @ 5V 1.375 @ 3.3V	128 / 128	Yes	Program / Program	Auto	Auto	Yes	Auto	Intel or PC COM Port	Auto Sleep	1.3	PLCC-44 TQFP-48	2.97V - 5.5V UART with 5V tolerant inputs. Similar to 650A with 128 bytes of FIFOs, programmable FIFO trigger levels, FIFO level counters, 16-level RTS flow control hysteresis, and RS485 half-duplex direction control output.
ST16C650A	1	3.0 @ 5V 2.0 @ 3.3V	32 / 32	No	4 Levels / 4 Levels	Auto	Auto	Yes	No	Intel or PC COM Port	Auto Sleep	1.3	PLCC-44 TQFP-48	2.9V - 5.5V UART with 5V tolerant inputs. Same as 650/550 with 32-byte FIFOs. Replaces 650, it saves the EFR register's settings and has automatic RTS flow control activation hysteresis.
XR16L651	1	3.0 @ 5V 2.0 @ 3.3V 1.0 @ 2.5V	32 / 32	No	4 Levels / 4 Levels	Auto	Auto	Yes	Yes	Intel or Motorola	Auto Sleep	1.3	TQFP-48	2.25V - 5.5V UART. Same features with 650A plus Intel or Motorola interface, has IrDA TX pulse width control, and sleep wake-up indicator.
XR16L570	1	3.0 @ 5V 2.0 @ 3.3V 1.5 @ 2.5V 0.75 @ 1.8V	16 / 16	No	4 Levels / 4 Levels	Auto	Auto	Yes	No	Intel	Power Save	3	QFN-24 QFN-32	1.62V - 5.5V UART with 5V tolerant inputs. Software compatible to industry standard 16550. PowerSave mode to further reduce power drain in sleep mode. Tiny 24-QFN and 32-QFN packages and other enhanced features.

EXAR UART and I/O SELECTOR GUIDE (Rev 2.20, August 2006)

UART DEVICE	Channels	Max Data Rate (Mbps)	TX/RX FIFO Bytes	TX & RX FIFO Level Counters	RX / TX FIFO INT Trigger	RTS/CTS Flow Control	S/W Flow Control	IrDA ver1.0 Endec	Auto RS485 HDX Control	Data Bus Interface	Power Down Mode	Max Icc (mA)	Packages	Other Features
XR16L580	1	3.0 @ 5V 2.0 @ 3.3V 1.5 @ 2.5V	16 / 16	No	4 Levels / 4 Levels	Auto	Auto	Yes	No	Intel or Motorola	Power Save	3	QFN-24 QFN-28 QFN-32 TQFP-48	2.25V - 5.5V UART with 5V tolerant inputs. Software compatible to industry standard 16550, Intel or Motorola mode, PowerSave mode to further reduce power drain in sleep mode. Tiny 24-QFN, 28-QFN and 32-QFN packages and other enhanced features.
ST16C580	1	1.5 @ 5V 0.5 @ 3.3V	16 / 16	No	4 Levels / 4 Levels	Auto	Auto	Yes	No	Intel	Auto Sleep	1.2	TQFP-48	2.97V - 5.5V UART. Same as 550 with auto hardware and software flow control, BRG prescaler, IrDA v1.0 endec and sleep mode.
ST16C550	1	1.5 @ 5V 0.5 @ 3.3V	16 / 16	No	4 Levels / No	No	No	No	No	Intel	No	1.2	PDIP-40 PLCC-44 TQFP-48	2.97V - 5.5V UART. Current PC standard UART. Pin-to-pin and function compatible to 450 plus 16 bytes of TX and RX FIFO, RX trigger levels.
ST16C450	1	1.5 @ 5V 0.5 @ 3.3V	1 / 1	No	No / No	No	No	No	No	Intel	No	1.2	PDIP-40 PLCC-44 TQFP-48	2.97V to 5.5V UART. Original PC standard serial port.
ST16C1550	1	1.5 @ 5V 0.5 @ 3.3V	16 / 16	No	4 Levels / 4 Levels	No	No	No	No	Intel	Yes	1.2	PLCC-28 TQFP-48	2.97V - 5.5V UART. Same as 1450 with 16-byte FIFOs, TXRDY & RXRDY Interrupt and RXRDY Time-out Interrupt features. Not compatible to Windows device drivers.
ST16C1551	1	1.5 @ 5V 0.5 @ 3.3V	16 / 16	No	4 Levels / 4 Levels	No	No	No	No	Intel	Yes	1.2	PLCC-28 TQFP-48	2.97V - 5.5V UART. Same as 1550 but has external clock instead of crystal and provides a Reset output. Not compatible to Windows device drivers.
ST16C1450	1	1.5 @ 5V 0.5 @ 3.3V	1 / 1	No	No / No	No	No	No	No	Intel	Yes	1.2	PLCC-28 TQFP-48	2.97V - 5.5V UART. Has soft-reset, tri-stateable interrupt select. Power down is immediate.

I/O PRODUCTS

PRINTER DEVICE	Channels									Host Interface	Vcc (Volt)	Temp Grade	Package	Other Features
ST78C34	1									Intel Bus	+5	C	PLCC-44 PDIP-40	Standard PC/Centronics Printer Port (SPP) Host device with 83-byte of FIFO and on-chip oscillator.
ST78C36	1									Intel Bus	+5	C	PLCC-44 LQFP-64	SPP/ECP/EPP (IEEE 1284) Printer Port Host device with 16-byte FIFO and printer port direction indicator. PLCC package provides Ext. Clock, 1 DMA & 1 Interrupt (H/W select). TQFP package provides on-chip oscillator, 3 DMA & 1 Interrupt (S/W select).
ST78C36A	1									Intel Bus	+5	C	PLCC-44	SPP/ECP/EPP (IEEE 1284) Printer Port Host device with 16-byte FIFO, on-chip oscillator, 1 DMA & 1 Interrupt (H/W select).